



**Shri Vaishnav Vidyapeeth Vishwavidyalaya**  
**Shri Vaishnav Institute of Technology and Science**  
**Choice Based Credit System (CBCS) Scheme in the light of NEP-2020**  
**Diploma in Electronics and Instrumentation Engineering**

**SEMESTER- II (2022-2025)**

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem Unversity Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem Unversity Exam (60%)	Teachers Assessment* (40%)	
1	DTMA201	Applied Mathematics II	3	1	0	4	60	20	20	0	0	100
2	DTCH101	Engineering Chemistry	2	1	2	4	60	20	20	30	20	150
3	DTEI201	Electronics and Instrumentation	3	0	0	3	60	20	20	0	0	100
4	DTEE101	Basic Electrical Engineering	2	1	2	4	60	20	20	30	20	150
5	DTMT202	Basic Electronics Engineering	2	1	2	4	60	20	20	30	20	150
6	DTHU101	Communication Skills	0	0	4	2	0	0	0	30	20	50
7	DTCS201	Computer Application-II	0	0	2	1	0	0	0	30	20	50
<b>TOTAL</b>			<b>12</b>	<b>4</b>	<b>12</b>	<b>22</b>	<b>300</b>	<b>100</b>	<b>100</b>	<b>150</b>	<b>100</b>	<b>750</b>

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

\*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

**Chairperson**  
Board of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Chairperson**  
Faculty of Studies  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Controller of Examinations**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Joint Registrar**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore

**Vice Chancellor**  
Shri Vaishnav Vidyapeeth  
Vishwavidyalaya, Indore